

MAR 08 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers Docket No: TI-30883  
Serial No: 09/902,051 Examiner: Davidson, Dan  
Filed: 07/10/01 Art Unit: 2651  
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION

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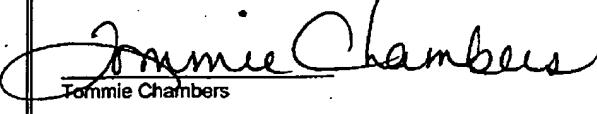
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RESPONSE

Assistant Commissioner For Patents  
Washington, DC 20231

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on 3-8-04:

  
Tommie Chambers

Dear Sir:

Responsive to the Office Action mailed December 8, 2003 in connection with the above identified application, Applicants respectfully submit the following remarks.

TI 30883 Page 1 of 3

To: Technology Unit: 2651  
Facsimile Number: 703-872-9306

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From: W. Daniel Swayze, Jr.  
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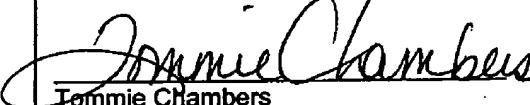
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NAME OF INVENTOR(S): Chambers	
TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION	
TI FILE NO.: <b>TI-30883</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: 03/08/2004 DUE: 02/08/2004 ATTY/SECY: WDS/lc	
RECEIPT DATE & SERIAL NO.: <b>Serial No.: 09/902,051</b> <b>Filing Date: 7/10/2001</b>	

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